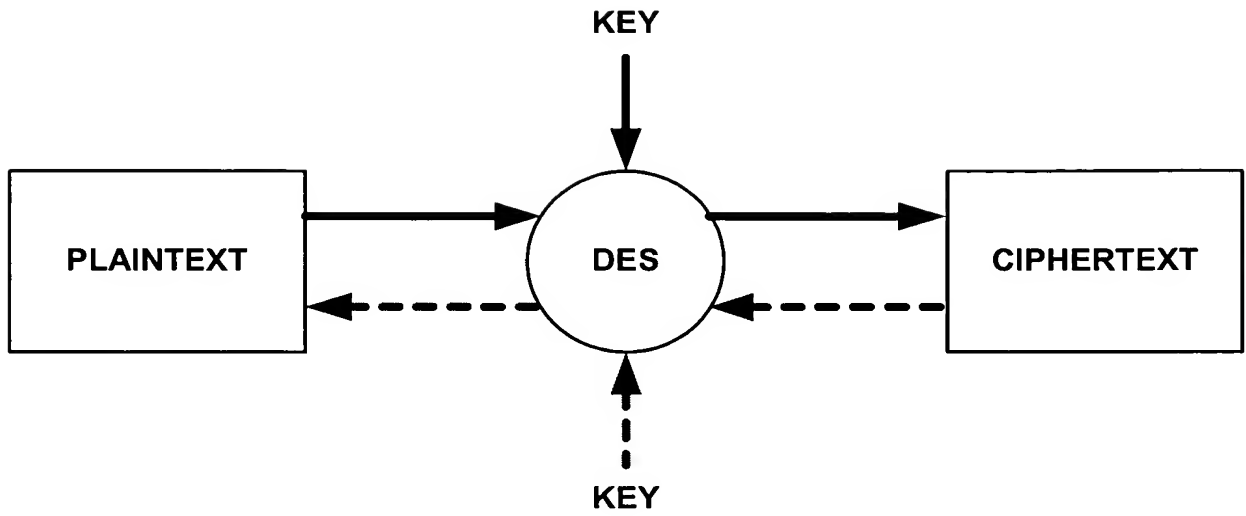
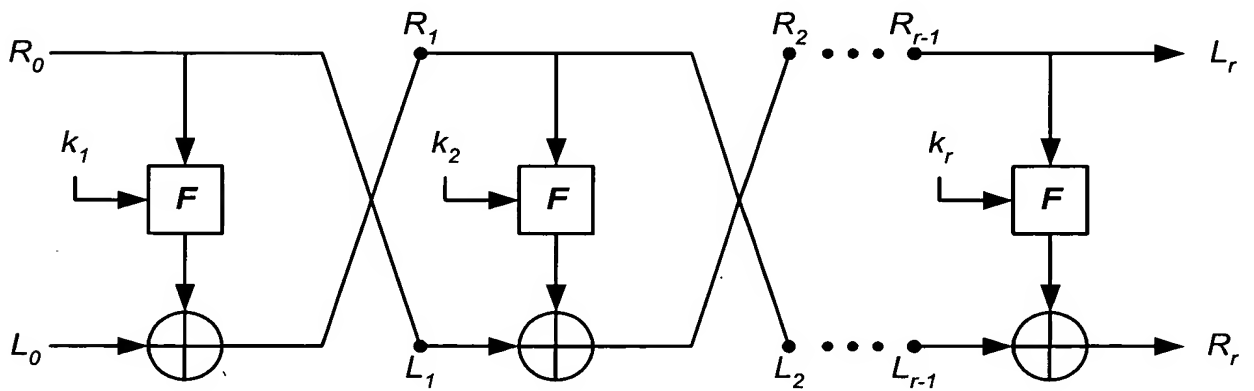


1 →

**Fig. 1A**

2 →

*Feistel Cipher***Fig. 1B**

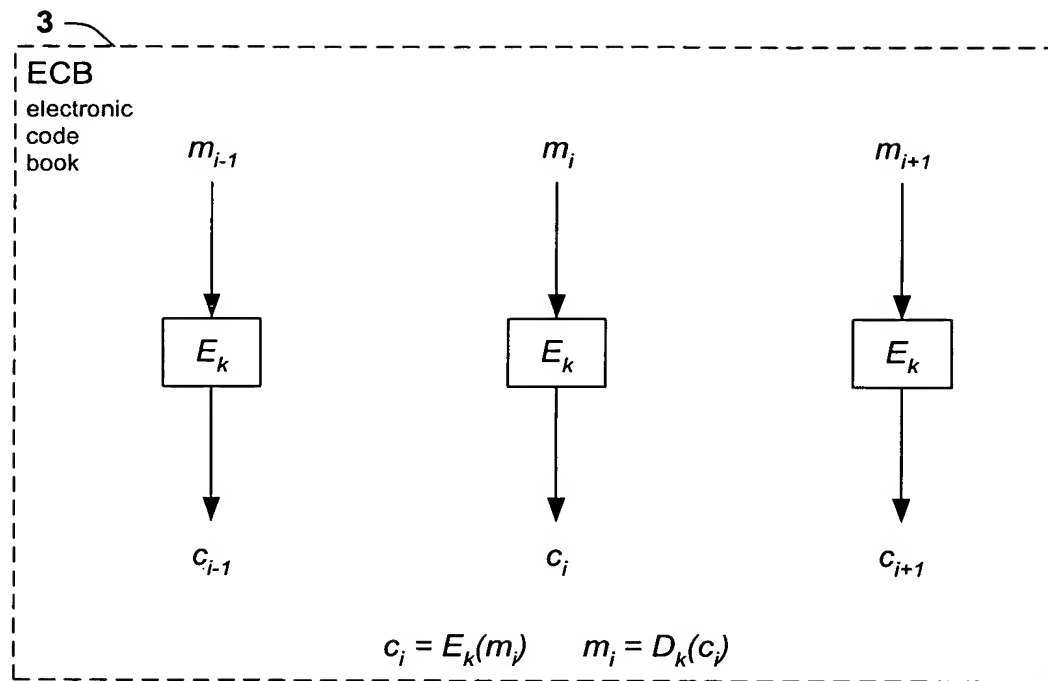


Fig. 1C

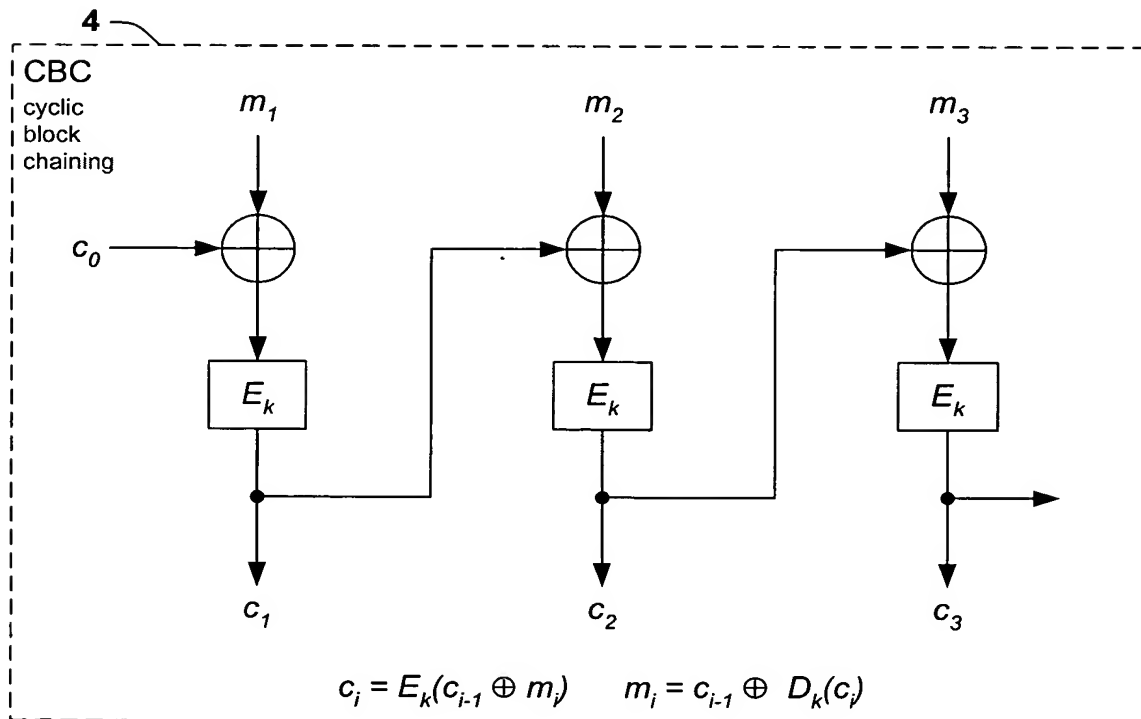
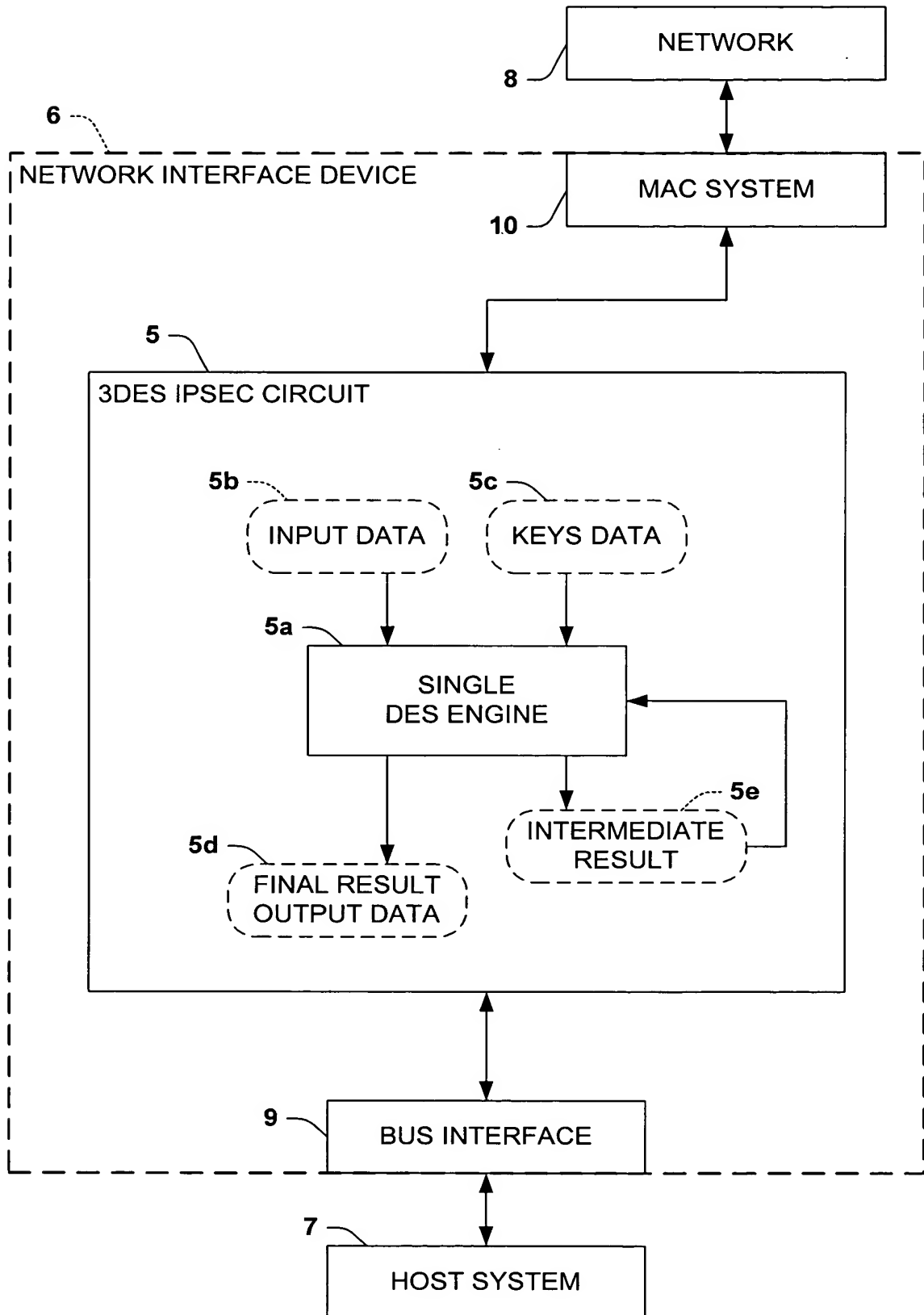
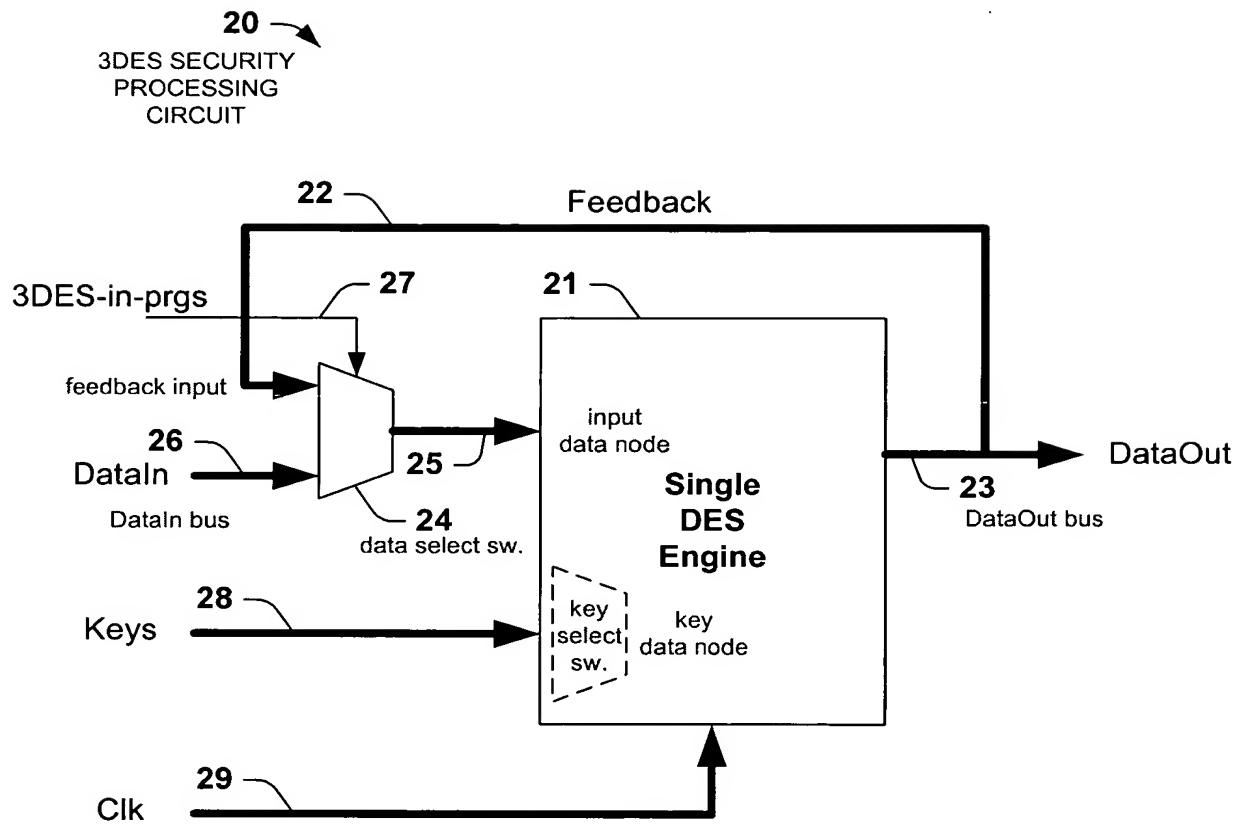


Fig. 1D

**Fig. 1E**

**Fig. 1F**

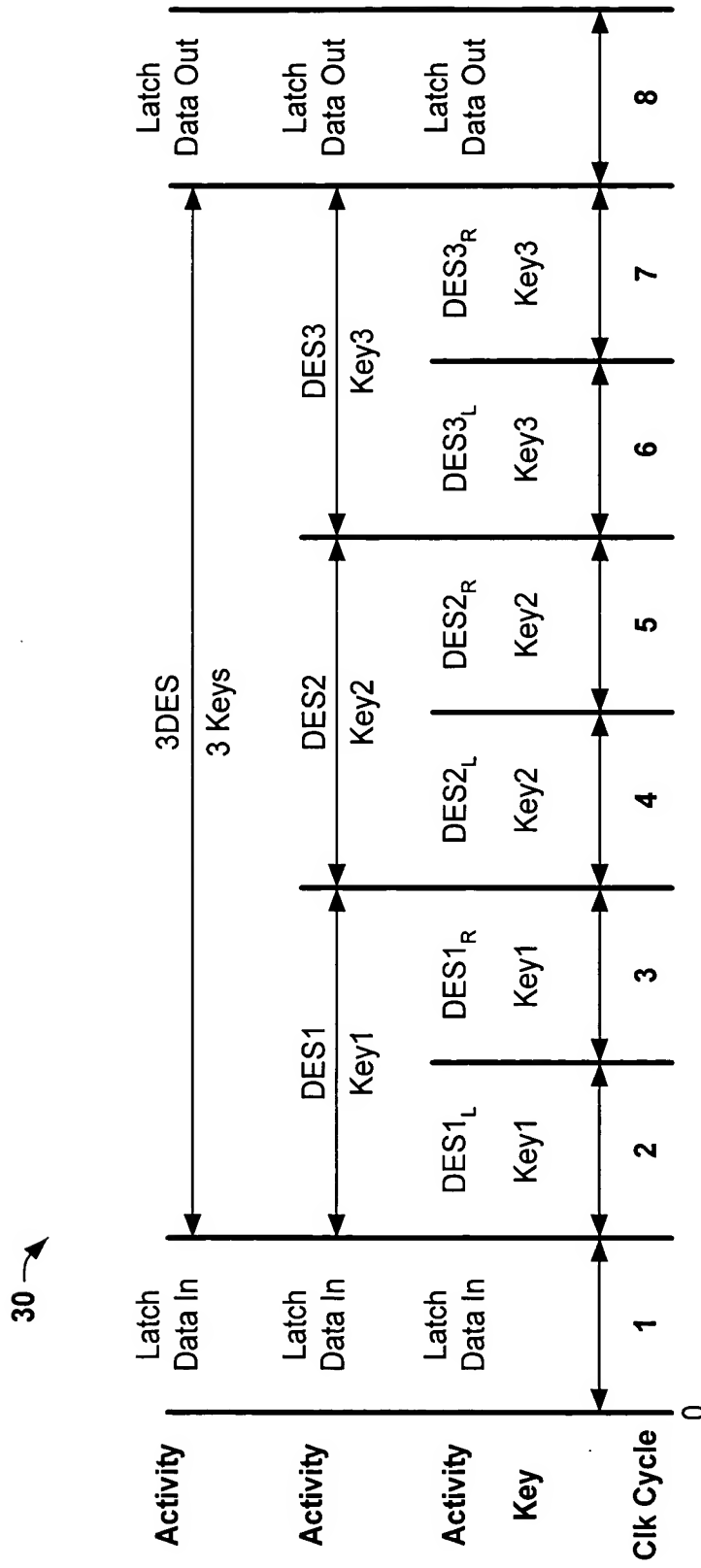
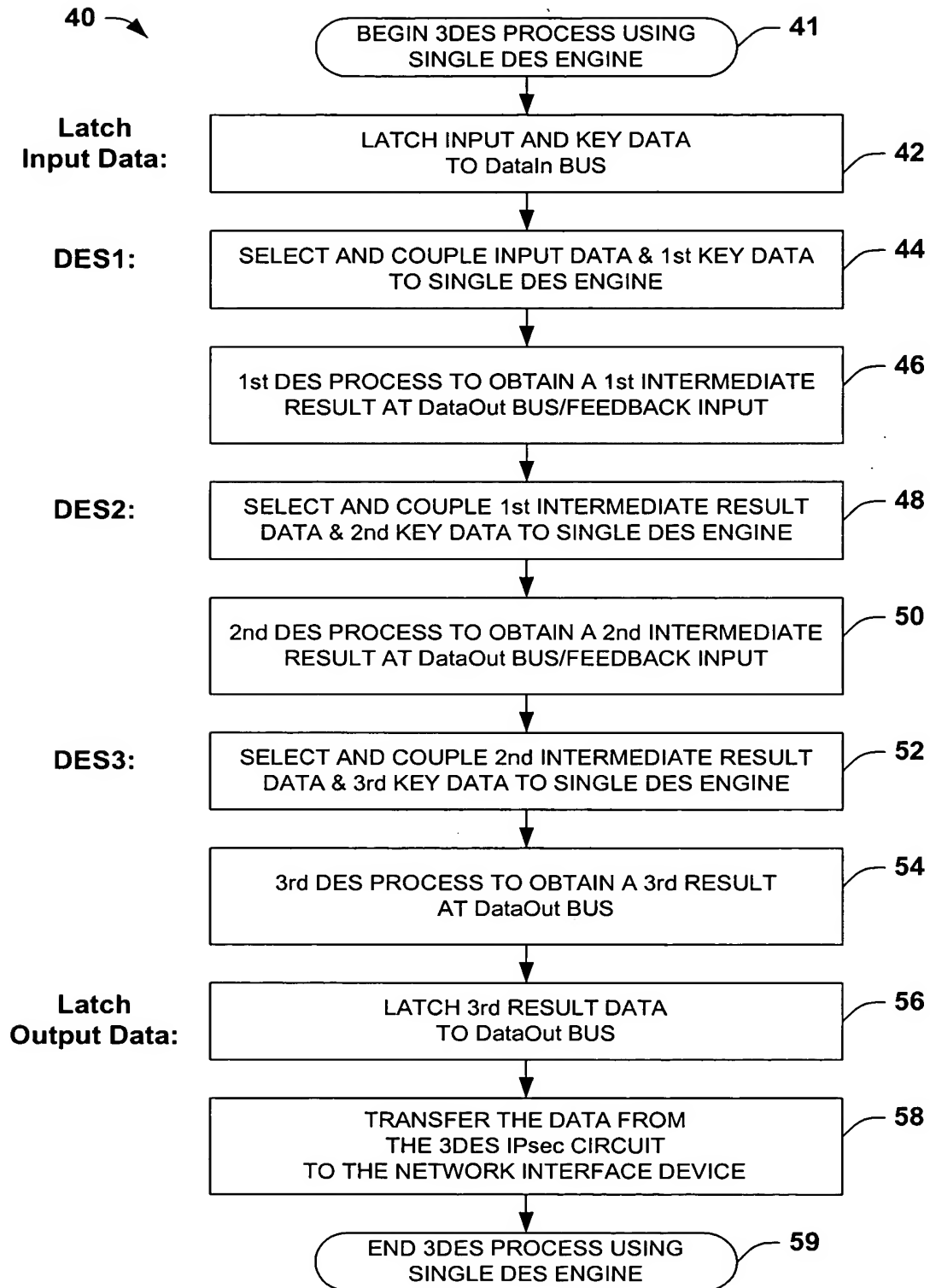


Fig. 1G

**Fig. 1H**

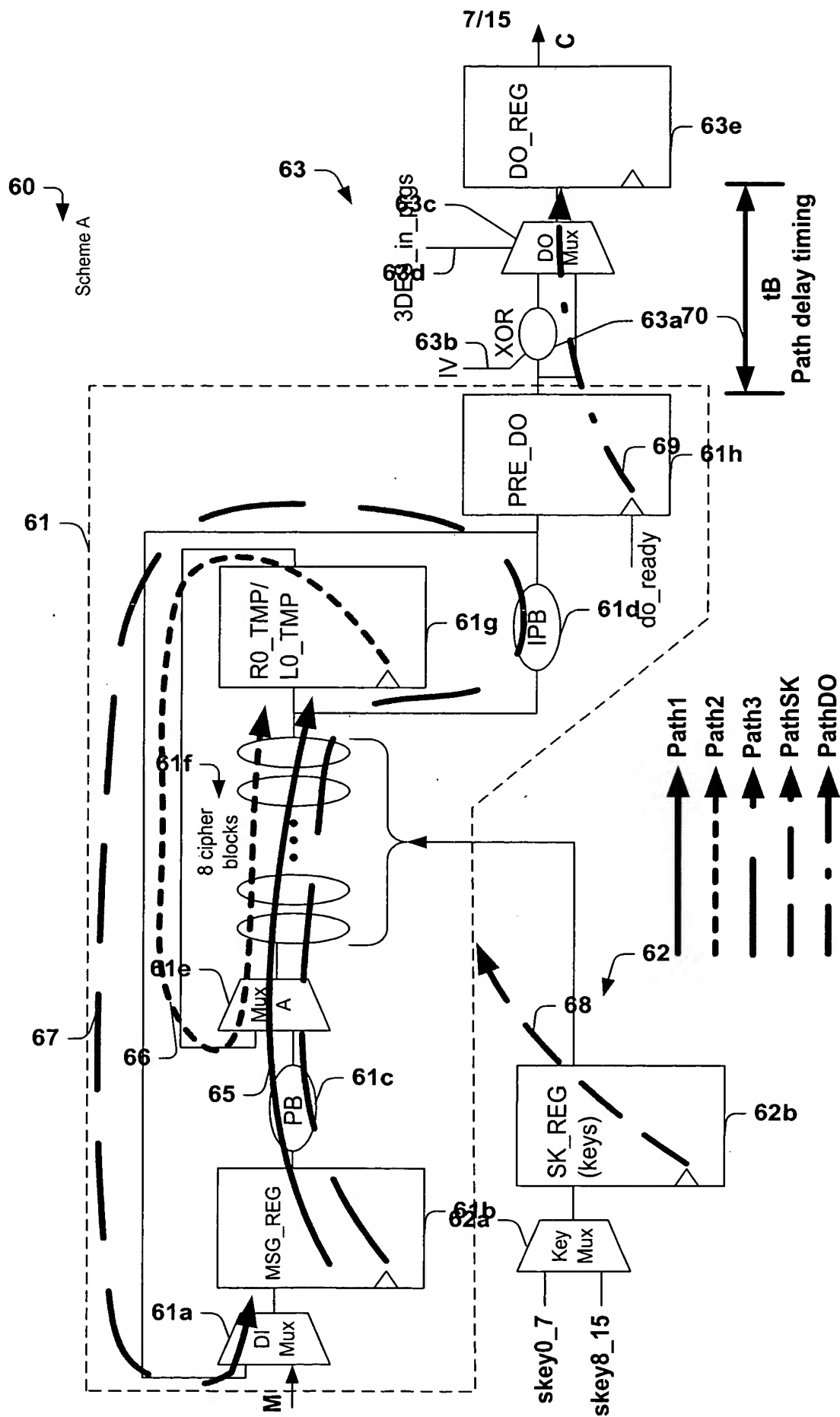
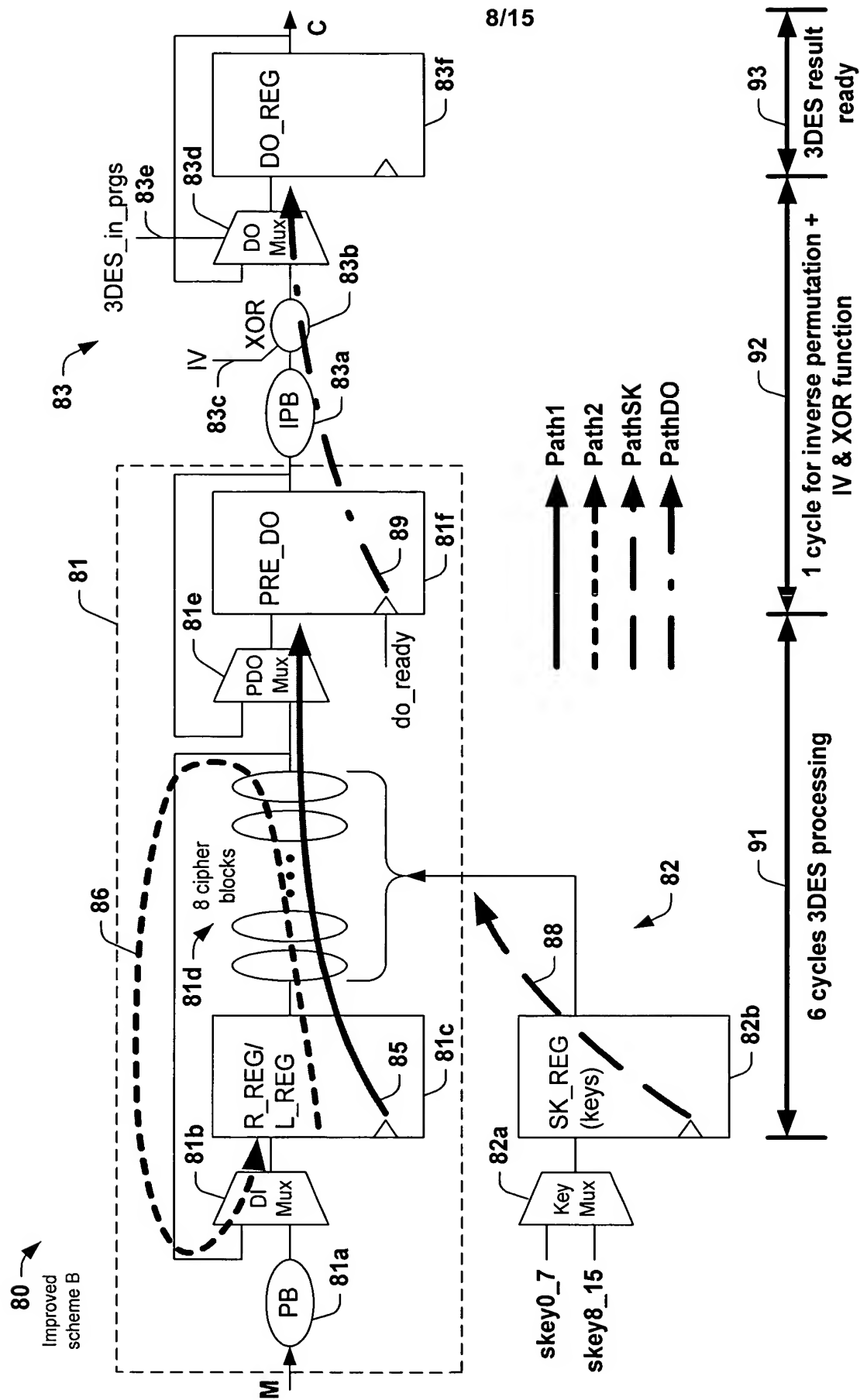


Fig. 11



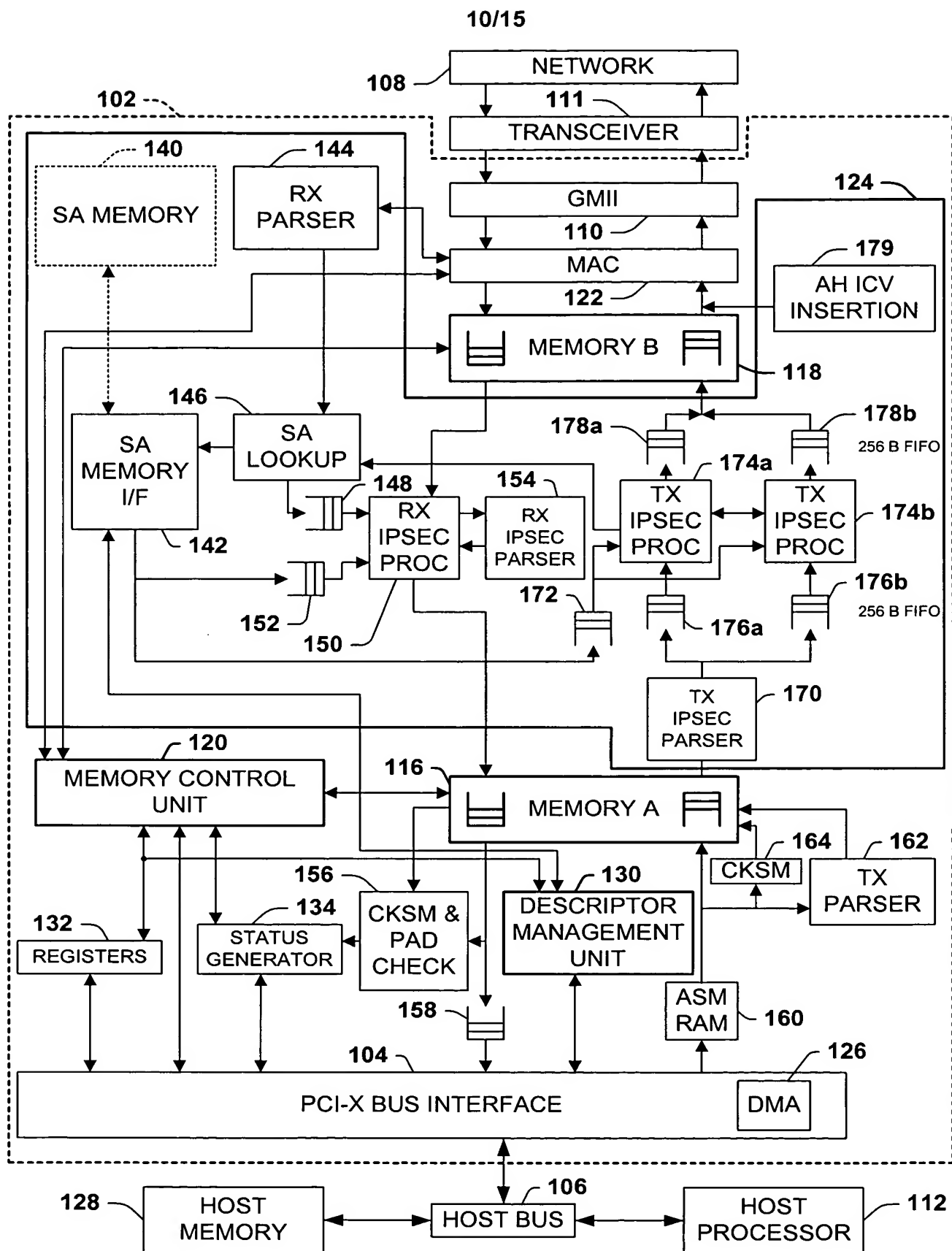
**Fig. 1J**



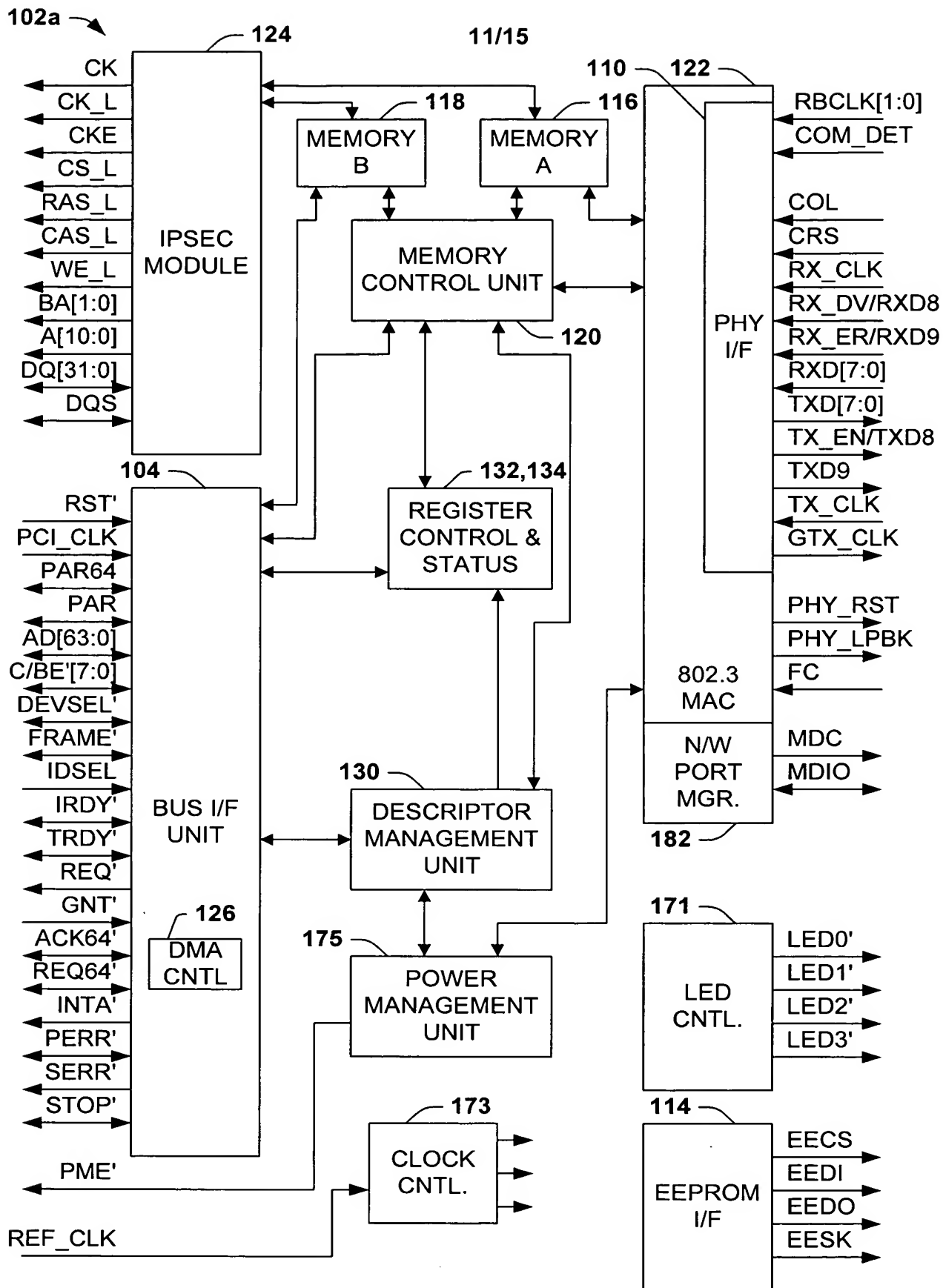
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<b>Scheme A</b>	<b>Improved Scheme B</b>
<b>Path1 (1st 8 steps)</b>	<b>Path1 (1st 8 steps)</b>
PB	8 Cipher Blks
MuxA	PDO Mux
8 Cipher Blks	
<b>Path2 (2nd 8 steps)</b>	<b>Path2 (2nd 8 steps)</b>
MuxA	8 Cipher Blks
8 Cipher Blks	DI Mux
<b>Path3 (loopback for 3DES)</b>	<b>Path3 (equivalent- 1 or 2)</b>
PB	8 Cipher Blks
MuxA	DI Mux or PDO Mux
8 Cipher Blks	
IPB	
DI Mux	
<b>PathDO</b>	<b>PathDO</b>
IV XOR	IPB
DO Mux	IV XOR
	DO Mux
<b>PathSK</b>	<b>PathSK</b>
8 Cipher Blks	8 Cipher Blks
	PDO Mux

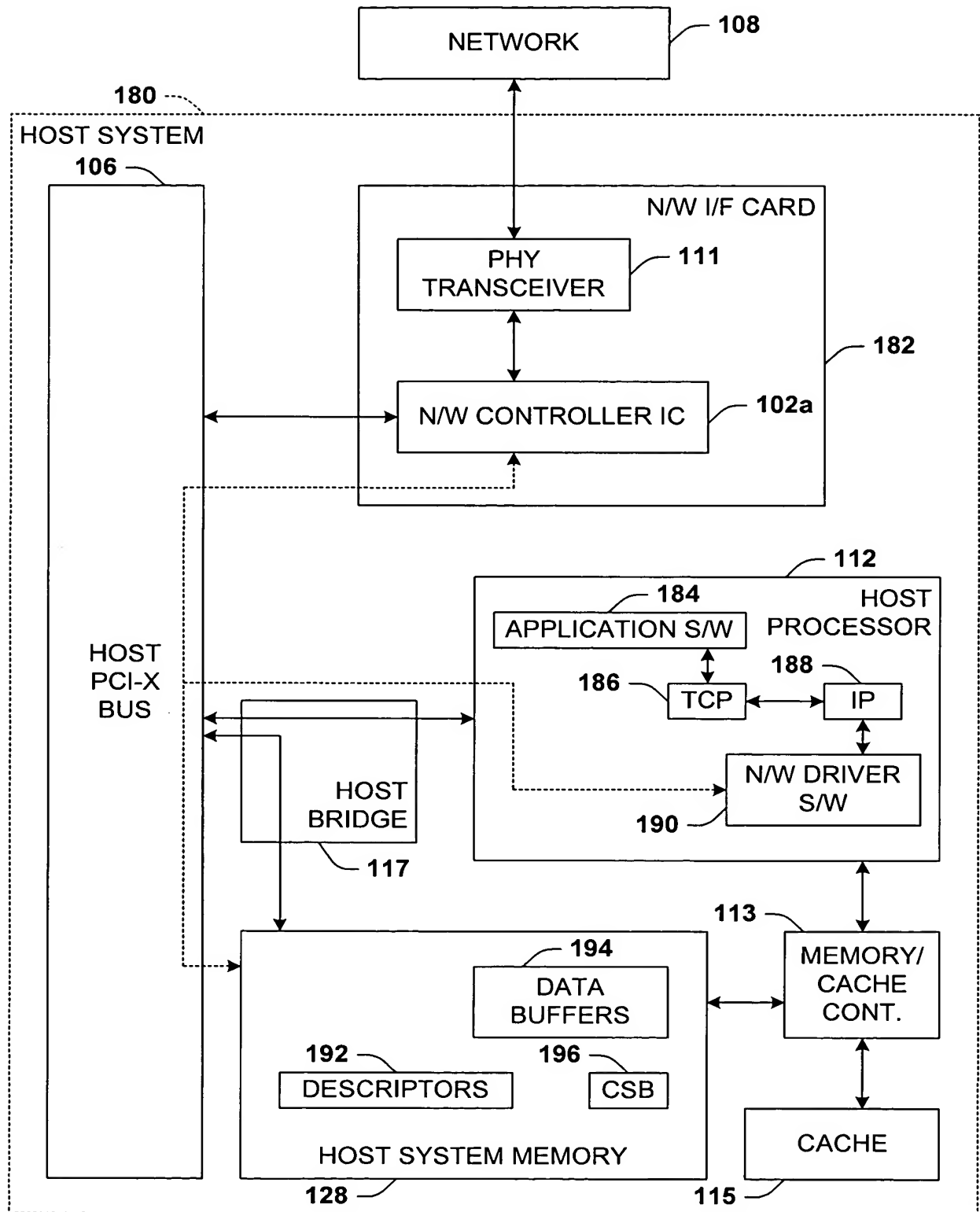
**Fig. 1K**

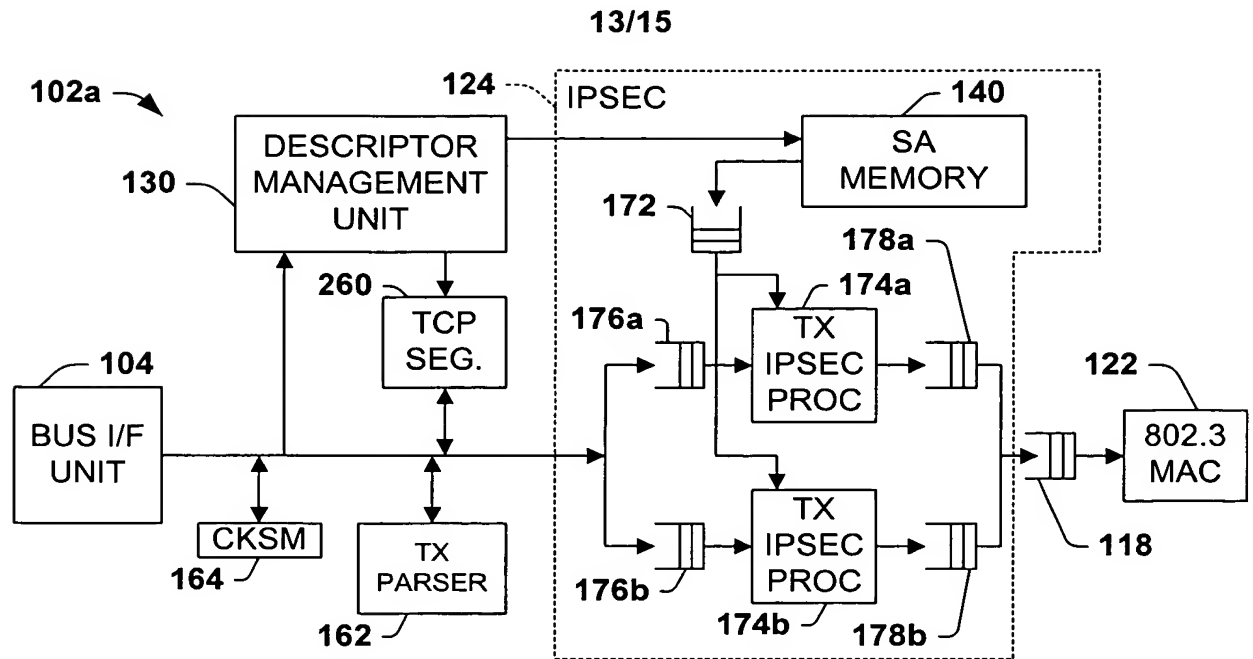


### Fig. 2

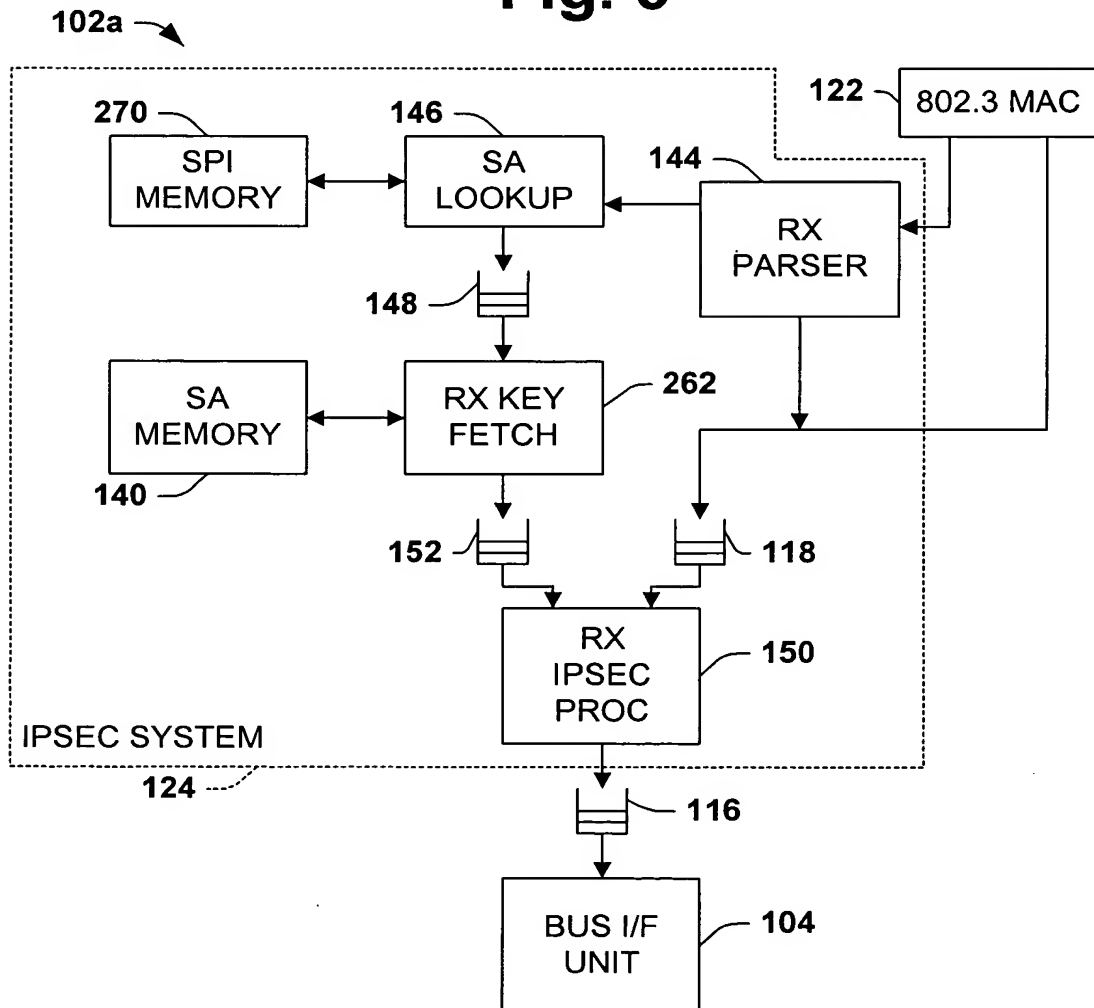


**Fig. 3**

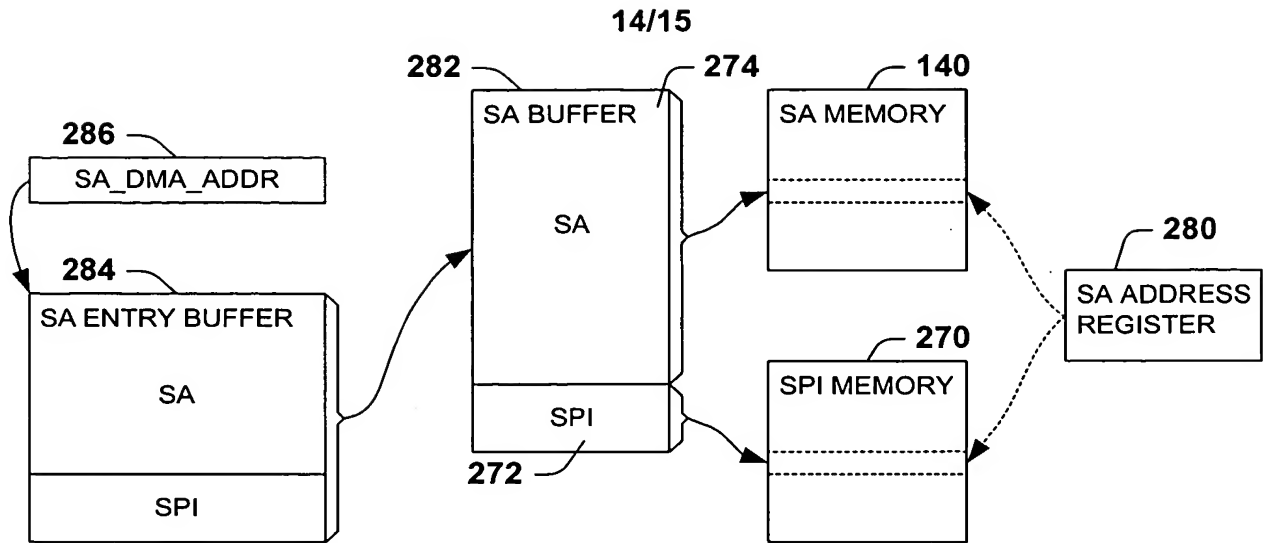
**Fig. 4**



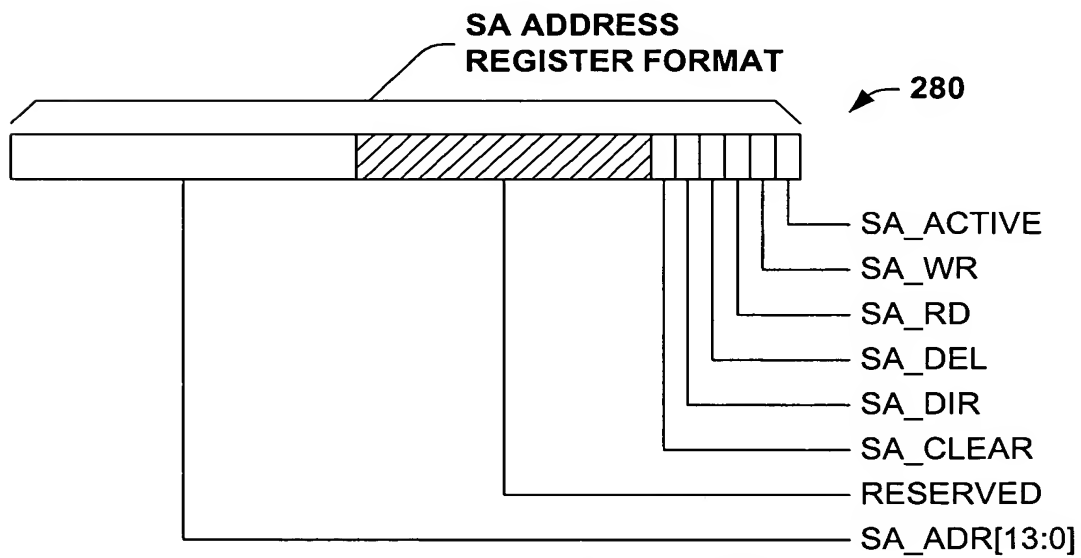
**Fig. 5**



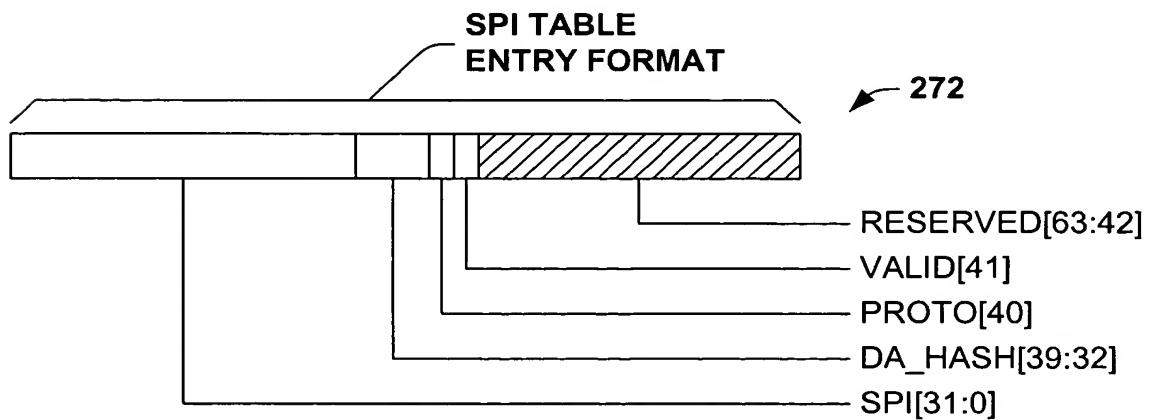
**Fig. 6**



**Fig. 7A**



**Fig. 7B**



**Fig. 7C**

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## SA MEMORY ENTRY

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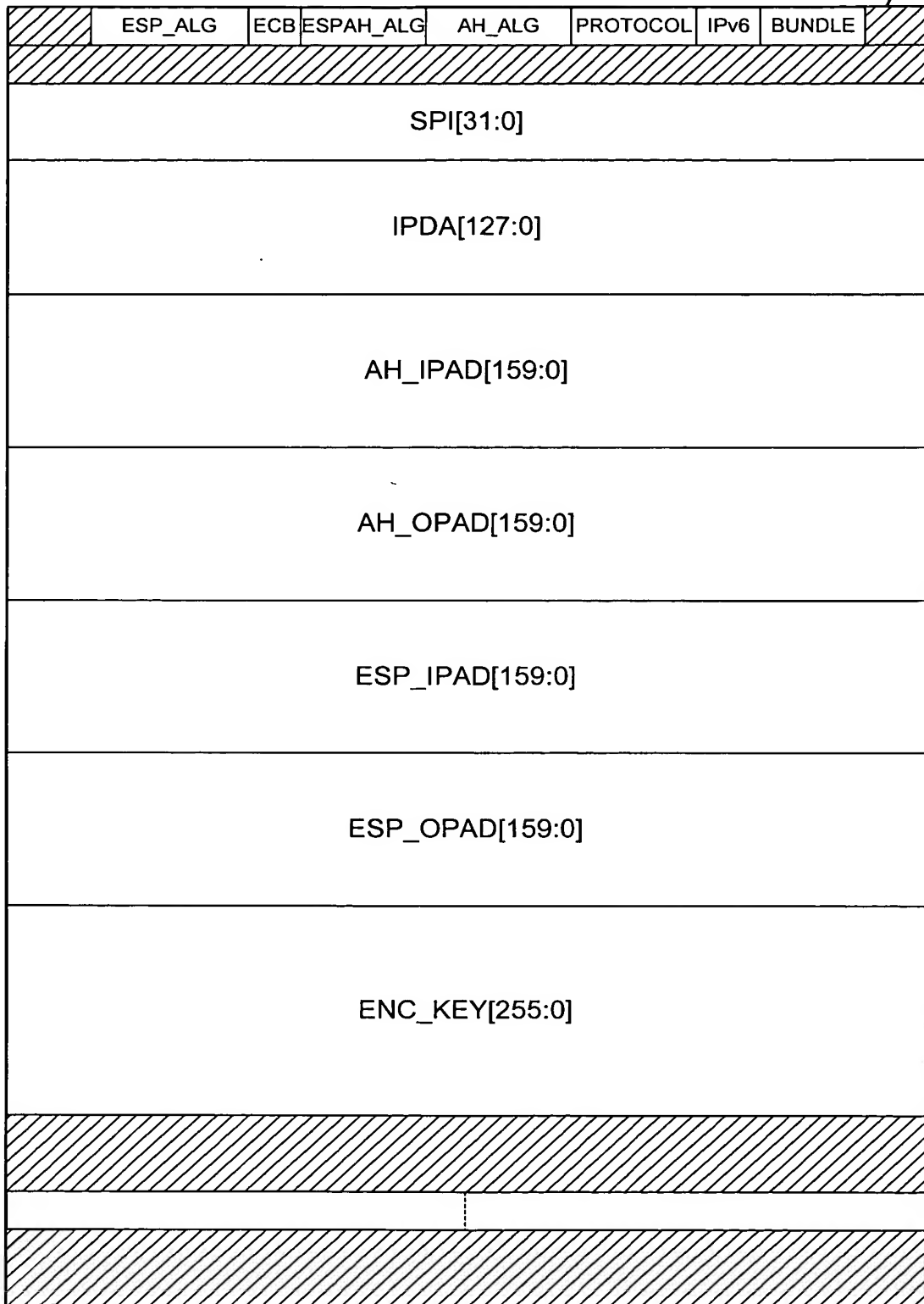


Fig. 7D